

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

Examination Section

M.Tech III Semester (R20/R21) Regular Examinations, November 2025

Timings: 10:00 a.m. to 1:00 p.m.

TIME TABLE

31st October 2025

Date	Structural Engineering	Power Electronics & Electric Drives	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning) (R21 Regulation)
10.11.2025 (Monday)	Design of Sub-structures (CE3922)	Smart Grid Technologies (EE3923)	Lower Power VLSI Design (EC4922)	Recommender Systems (CS4923)
12.11.2025 (Wednesday)	Sustainable Development (CE3924)			

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.


Controller of Examinations


Principal

Copy to: HODs – CE/EEE/ECE/CSE for Notice Board display/Exam Section
Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)